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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	1	Application Number	10/750,715
				Filing Date	12-31-2003
				First Named Inventor:	Newburn
				Art Unit	2187
				Examiner Name	Farrokh, H.
				Attorney Docket Number	42P18598

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		ABALI, B., et al., IBM Research Report "Performance of Hardware Compressed Main Memory", RC 21799 (98138) 19 July 2000, Computer Science/Mathematics, Research Division, Cover page plus pages 1-12, IBJ T.J. Watson Research Center, P.O. Box 218, Yorktown Heights, NY 10598.	
		ALAMELDEEN, A., "Adaptive Cache Compression for High-Performance Processors", 31 <sup>st</sup> Annual International Symposium on Computer Architecture (ISCA-31), Munich Germany, June 19-23, 2004, pages 1-12.	
		ABALI, B., et al., "Performance of Hardware Compressed Main Memory", 9 pages, IBM T.J. Watson Research Center, P.O. Box 218, Yorktown Heights, NY 10598.	
		ABALI, B., et al., "Memory Expansion Technology (MXT): Software Support And Performance", IBM J. Res. & Dev., Vol. 45, No. 2, March 2001, pages 287-301.	
		FRANASZEK, P.A., et al., "Algorithms And Data Structures For Compressed-Memory Machines", IBM J. Res. & Dev., Vol. 45, No. 2, March 2001, pages 245-258.	
		ABALI, B., et al., "Hardware Compressed Main Memory: Operating system Support and Performance Evaluation", IEEE Transactions on Computers, Vol. 50, No. 11, November 2001, pages 1219-1229	
		HALLNOR, E., et al., "A Compressed Memory Hierarchy Using An Indirect Index Cache", 2004, Cover Sheet plus pages 2-17, Advanced Computer Architecture Laboratory, EECS Department, University of Michigan, Ann Arbor, MI 48109-2122.	

Examiner Signature	/Hashem Farrokh/	Date Considered	06/27/2008
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /H.F./

Based on Form PTO/SB/08B (08-03) as modified by BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP on 09/10/03.